



**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE**  
(UGC - AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)

www.mits.ac.in

**Examination Section**

**M.Tech – I Year I Semester (R14) Regular & Supplementary End Semester Examinations February/March – 2016**  
Academic Year – 2015 – 16 (2014 Admitted batch onwards)

**TIME TABLE**

**TIMINGS: FN: 10:00 AM to 01:00PM**

DATE / DAY	Electrical Power Systems (EPS)	Solar Power Systems (SPS)	Advanced Manufacturing Systems (AMS)	Digital Electronics & Communication Systems (DECS)	VLSI & Embedded Systems (VES)	Computer Science & Engineering (CSE)
22.02.2016 Monday	Modern Control Theory 14EPS11T01	Modern Control Theory 14SPS11T04	Automation in Manufacturing 14AMS11T01	Structural Digital Systems Design 14DECS11T01	Structural Digital Systems Design 14VES11T03	Programming Using Python 14CSP11T01
24.02.2016 Wednesday	Power System Control & Stability 14EPS11T02	Basis of Solar Cell Devices 14SPS11T01	Advanced Manufacturing Processes 14AMS11T02	Advanced Computer Networks 14DECS11T02	VLSI Technology 14VES11T01	Advanced Data Structures and Algorithms 14CSP11T02
26.02.2016 Friday	Operation and Control of Power System 14EPS11T03	Power Electronic Converters 14SPS11T02	Material Technology 14AMS11T03	Digital Communication Techniques 14DECS11T03	Embedded System Concepts 14VES11T02	Computer System Design 14CSP11T03
29.02.2016 Monday	Integration Of Renewable Energy Sources 14EPS11T04	Solar PV Technology 14SPS11T03	Product Design and Development 14AMS11T04	Advanced Digital Signal Processing 14DECS11T04	Analog & Digital IC Design 14VES11T04	Advance Databases 14CSP11T04
02.03.2016 Wednesday	Analysis of Power Electronic converters 14EPS11T05	Energy Storage in Solar Power System 14SPS11T05	Theory of Metal Cutting & Tool Design 14AMS11T05	Adaptive Signal Processing 14DECS11T05	Hardware Description Languages 14VES11T05	Object Oriented Analysis Design and Design Pattern 14CSP11T05
04.03.2016 Friday	Artificial Intelligence Techniques 14EPS11E1a	Artificial Intelligence Techniques 14SPS11E1c	Advanced Computer Aided Design 14AMS11E1b	Mobile Networks 14DECS11E1c	MEMS 14VES11E1a	Software Engineering 14CSP11E1a

**Note:** (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.  
(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

*S. Kumar V*  
**CONTROLLER OF EXAMINATIONS**

**Date: 08.02.2016**

Copy to: The Principal

All the HODs for circulation among the staff members and display on notice board,  
The Examination Section,  
The Senior Administrative Officer,  
The Placement Officer.

*8/2/16*  
*E.E. ME. E.E. CSE*

*Cyji*

**PRINCIPAL**

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